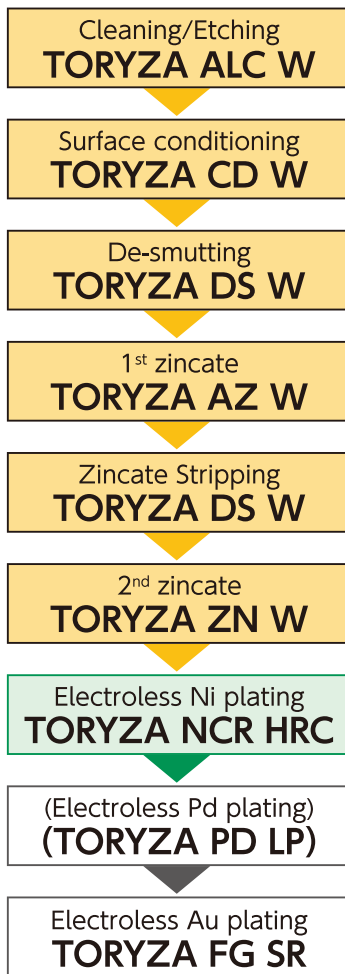


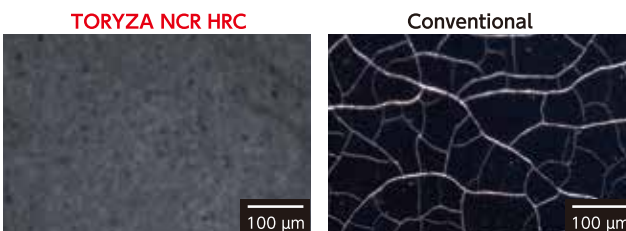
# UBM formation on aluminum electrodes on wafer

## TORYZA EL PROCESS



## TORYZA NCR HRC

For high temperature jointing



Indentation test by Erichsen tester  
(Ni thickness 3 μm, indentation width : 0.5 mm)

**Prevent cracks after 400 °C heat treatment**

For high temperature use

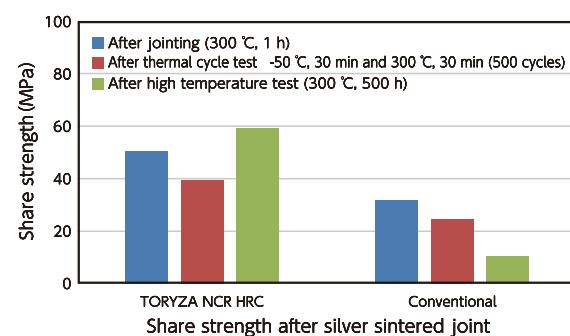
Crack occurrence comparison of electroless Ni plating film

Electroless Ni plating film	Crack occurrence		
	After heat treatment	After thermal cycle	After high temperature test
<b>TORYZA NCR HRC</b>	No	No	No
Conventional	No	Occur	Occur

Presented by SANKEN,  
Osaka University Flexible 3D JISSO  
Collaborative Research Institute

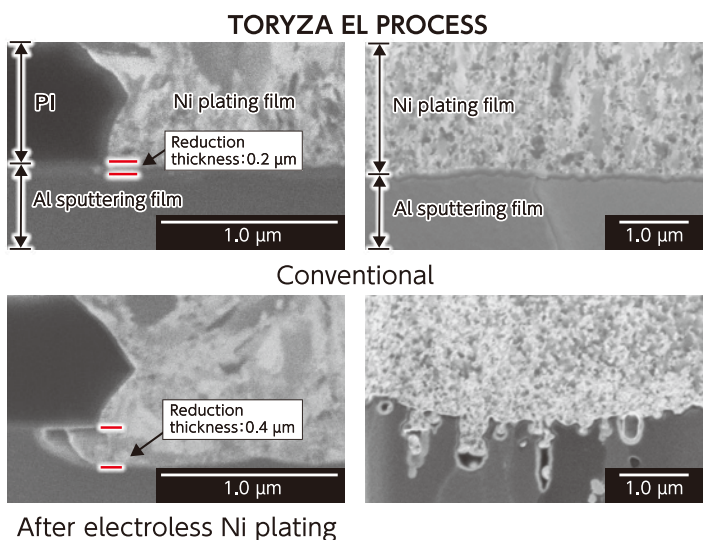
SiC Chip: Ti sputtering on SiC chip, and form Ag sputtering layer

Jointing condition : Electroless Ni plating (7 μm thickness) on DBA substrate and sintering SiC chip on DBA substrate with Ag paste (Sintering condition: 1 MPa, 300 °C, 1 h)



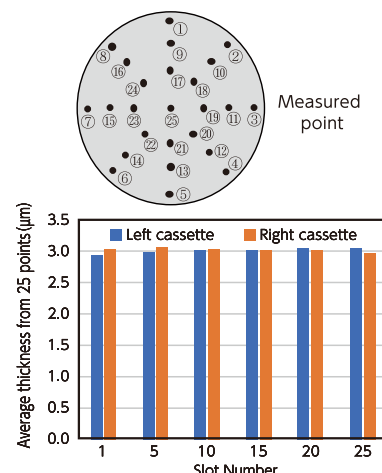
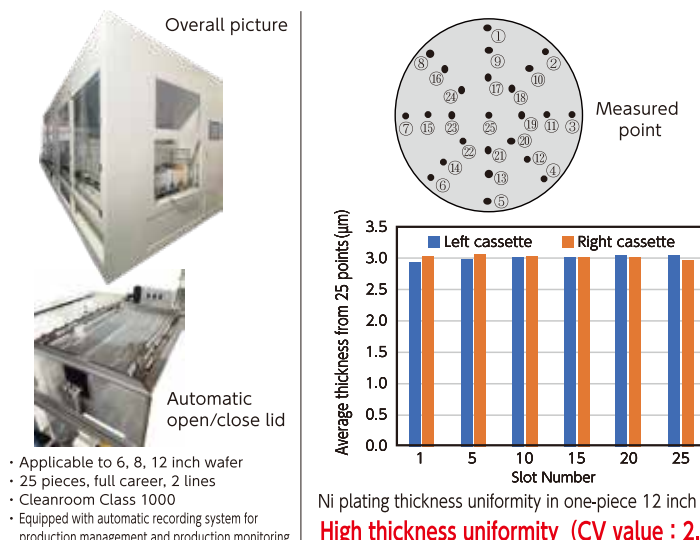
## TORYZA EL PROCESS

Reduce etching amount and local corrosion of Al sputtering film



## TORYZA EL SYSTEM

Automatic electroless plating equipment Applicable to 12 inch wafer



Ni plating thickness uniformity in one-piece 12 inch wafer  
**High thickness uniformity (CV value : 2.0 %)**



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