### TOP UBP PROCESS W

## Under barrier metal formation process for Al electrode on wafer

- Make zincate film finely, improve the smoothness of nickel film
- Prevent local corrosion and nickel spike in pre-treatment process for Al sputtering layer
- Electroless Ni plating: Prevent cracks even after heat treatment at 400°C
- High-temperature reliability for silver sintered jointing

Cleaning, etching
TOP UBP CLEAN W2K

Conditioning
TOP UBP CONDITIONER W

Desmutting
TOP UBP-DSW

1<sup>st</sup> zincate **TOP UBP-AZW2** 

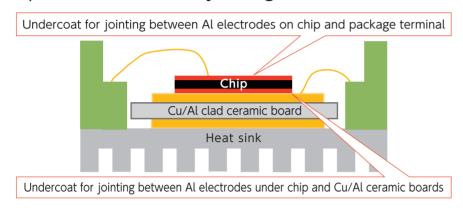
Zincate stripping **TOP UBP-DSW** 

2<sup>nd</sup> zincate **TOP UBP-ZNW2** 

Electroless Ni plating
TOP UBP NICORON HRC

(Electroless Pd plating)
(TOP UBP-PDL)

TOP UBP GOLD SR

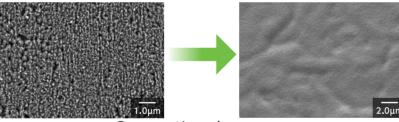


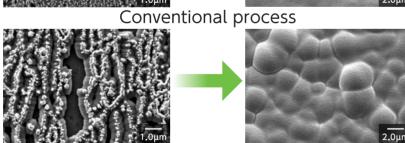
#### Fine, uniform film on Al electrodes

SEM surface image

After 2<sup>nd</sup> zincate After Electroless Ni plating

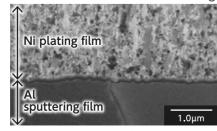
TOP UBP PROCESS W

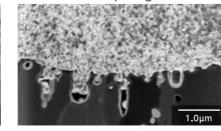




#### Prevent local corrosion of Al sputtering layer

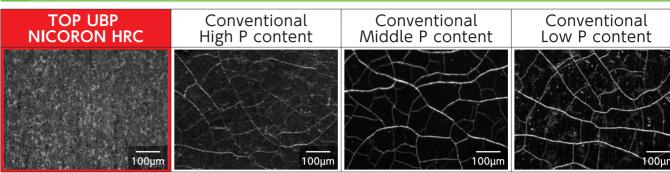
SEM cross section image after electroless Ni plating





TOP UBP PROCESS W Conventional process

#### **Electroless Ni plating film applicable to high-temperature jointing**



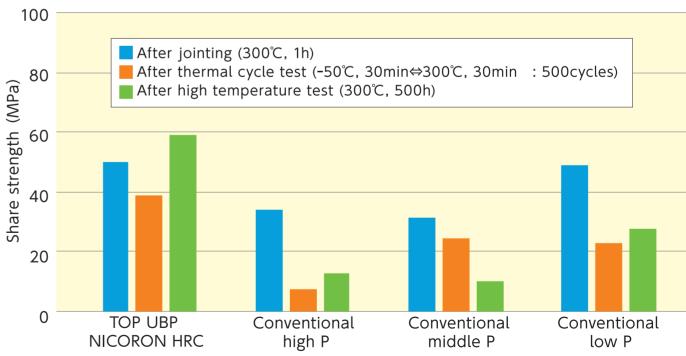
Indentation test by Erichsen tester (Ni thickness 3μm, indentation width: 0.5mm)

### Prevent cracks after 400°C heat treatment

# Electroless Ni plating film applicable to use under high-temperature environment

Crack occurrence comparison of electroless Ni plating film

Electroless Ni plating film	Crack occurrence		
	After jointing	After thermal cycle	After high temperature test
TOP UBP NICORON HRC	No	No	No
Conventional, high P	No	Occur	Occur
Conventional, middle P	No	Occur	Occur
Conventional, low P	No	Slightly occur	Slightly occur



#### Share strength after silver sintered joint

Presented by SANKEN, Osaka University Flexible 3D JISSO Collaborative Research Institute SiC Chip: Ti sputtering on SiC chip, and form Ag sputtering layer

Jointing condition: Electroless Ni plating ( $7\mu m$  thickness) on DBA substrate and sintering SiC chip on DBA substrate with Ag paste Adding 1MPa pressure, 300°C, 1h